

Figure 13

Figure 14 is a schematic diagram of a multi-output current mirror circuit. The circuit is powered by a positive supply voltage V_{cc} and a negative supply voltage V_{ee} . It consists of ten MOSFETs, labeled M1 through M10, and a diode D1. The MOSFETs are arranged in five pairs: (M1, M6), (M2, M7), (M3, M8), (M4, M9), and (M5, M10). Each pair is connected to a common source node, which is in turn connected to a current source I_1 through I_5 respectively. The gates of all MOSFETs are connected to a common gate node, which is also connected to V_{cc} . The drains of M1 and M6 are connected to the $OUT1+$ and $OUT1-$ outputs, respectively. The drains of M2 and M7 are connected to the $OUT2+$ and $OUT2-$ outputs, respectively. The drains of M3 and M8 are connected to the $OUT3+$ and $OUT3-$ outputs, respectively. The drains of M4 and M9 are connected to the $OUT4+$ and $OUT4-$ outputs, respectively. The drains of M5 and M10 are connected to the $OUT5+$ and $OUT5-$ outputs, respectively. A diode D1 is connected between V_{cc} and the $OUT5+$ output, with its cathode to V_{cc} and its anode to $OUT5+$. The current I_{out} is shown flowing out of the $OUT5+$ output.

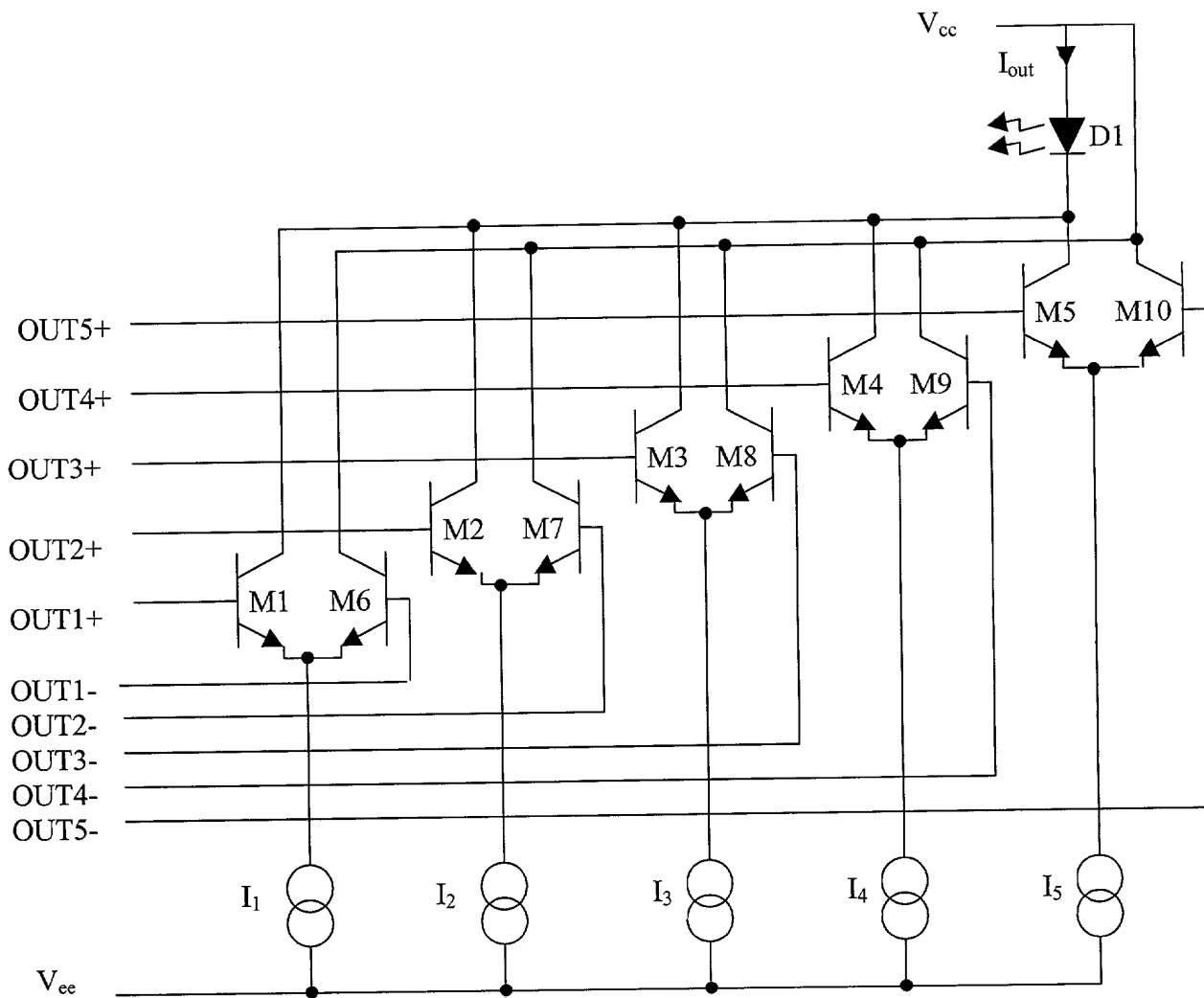


Figure 14

Figure 15

